## ABSTRACT OF THE DISCLOSURE

In a flat panel display device in which driving circuits and the like are arranged on an array substrate, a technology to realize a compact size and a low cost of an external control circuit is disclosed. At least one pumping circuit included in the external control circuit is arranged on the array substrate, and an output side capacitor connected between an output portion of the pumping circuit and the ground GND and an input side capacitor of a clock input portion are arranged outside the array substrate.

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